

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2137

Examiner: Matthew Smithers

Serial No.: 10/676,390

Filed: September 30, 2003

In re Application of: Biederman et al.

For: METHOD AND APPARATUS OF INTEGRATING LINK LAYER  
SECURITY INTO A PHYSICAL LAYER TRANSCEIVER

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**APPLICANT'S COMMENTS ON REASONS FOR ALLOWANCE**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

Dear Sir:

These comments are responsive to the Notice of Allowance mailed on August 9, 2007, which sets forth the Examiner's statement of reasons for allowance.

The Examiner is thanked for the kind allowance of all pending claims. Applicants acknowledge the Examiner's statement of reasons for allowance as set forth in the Notice of Allowance. However, Applicants point out that the reasons for allowability of the above referenced claims are not limited to the reasons for allowance as set forth in the Notice of Allowance, and that additional reasons for allowability may exist, each of which may be independently sufficient to establish the patentability of one or more pending claims.

Applicants respectfully reserve the right to introduce, articulate, or otherwise comment on any such additional reasons for allowance as may be appropriate in any future proceedings concerning the claimed invention.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-0612.

Respectfully submitted,  
SIERRA PATENT GROUP, LTD.

Dated: November 7, 2007

/kenneth d'alessandro/

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